

Tabelle1

|    |    |
|----|----|
| 15 | 14 |
| 16 | 13 |

FPGA 8

|    |    |
|----|----|
| 11 | 10 |
| 12 | 9  |

FPGA 2

|   |   |
|---|---|
| 7 | 6 |
| 8 | 5 |

FPGA 7

Bottom view of the board  
Top view of the board

The wafer attached to the board is rotated by 45 degrees

Number of reticle on the board (x/y position into the board)

|    |    |
|----|----|
| 18 | 17 |
| 19 | 20 |

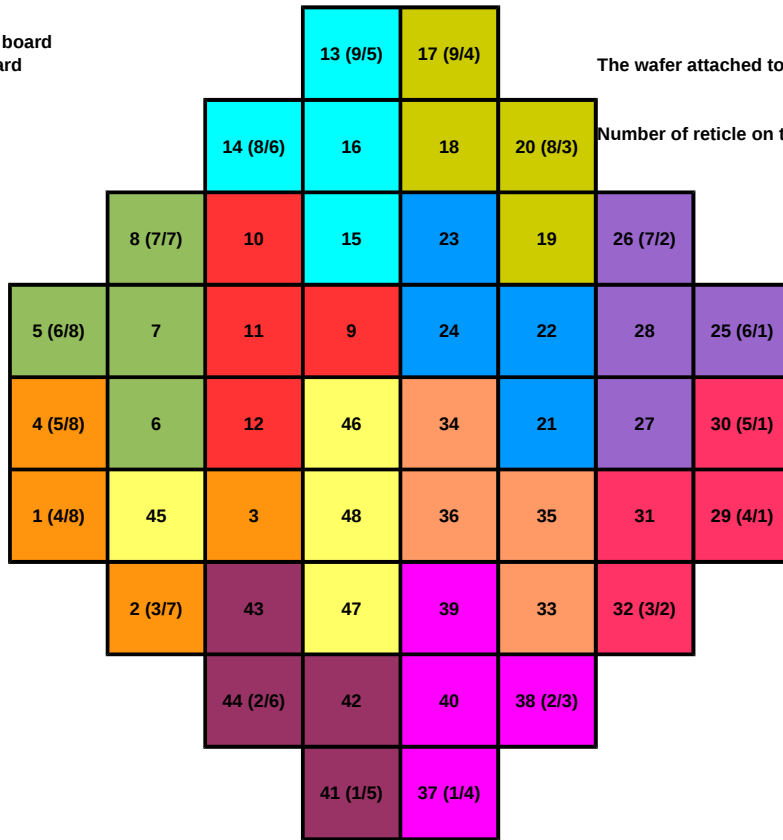
FPGA 9

|    |    |
|----|----|
| 46 | 45 |
| 47 | 48 |

FPGA 3

|    |    |
|----|----|
| 26 | 25 |
| 27 | 28 |

FPGA 10



wafer notch

|   |   |
|---|---|
| 4 | 3 |
| 1 | 2 |

FPGA 6

|    |    |
|----|----|
| 24 | 23 |
| 21 | 22 |

FPGA 1

|    |    |
|----|----|
| 44 | 43 |
| 41 | 42 |

FPGA 5

|    |    |
|----|----|
| 29 | 32 |
| 30 | 31 |

FPGA 11

|    |    |
|----|----|
| 33 | 36 |
| 34 | 35 |

FPGA 4

|    |    |
|----|----|
| 37 | 40 |
| 38 | 39 |

FPGA 12